EPSON

E0C63256

4-bit Single Chip Microcomputer

Preliminary

- 4-bit E0C63000 Core CPU
- A/D Converter
- Built-in LCD Driver
- High Speed Instruction Cycle (2-6CPI)

■ DESCRIPTION

The E0C63256 is a microcomputer composed of a CMOS 4-bit core CPU (E0C63000), ROM, RAM, LCD driver and timers. Since the E0C63000 core CPU executes the principle instructions in 1 or 2 cycles, it is suitable for various high-speed controller. Furthermore, the E0C63256 has built-in four-channel 8-bit A/D converter and is most suitable for applications such as control units for household electric appliances which need A/D conversion and liquid crystal display.

■ FEATURES

- CMOS LSI 4-bit parallel processing
- Clock ..... 0.5MHz to 4.5MHz (Ceramic, X'tal or CR oscillation)
- Instruction set ..... 46 types (411 instructions with all)
- ROM capacity ..... 6,144 words × 13 bits
- RAM capacity ..... 256 words × 4 bits
- Input port ..... 4 bits
- Output port ..... 4 bits
- I/O port ..... 8 bits
- LCD driver ..... 20 segments × 4/3/2 commons
- Clock timer ..... 1 ch.
- Programmable timer ..... 8 bits × 4 ch., 8 bits × 2 ch. and 16 bits × 1 ch. (Selective)
- Watchdog timer ..... Built-in
- A/D converter ..... 8 bits × 4 ch.
- Operation voltage ..... 2.7 to 5.5V
- Current consumption ..... 560μA (4.193MHz, X'tal, HALT)
  1.4mA (4.193MHz, X'tal, RUN)
  60μA (LCD current, 3V) ∗1
  100μA (LCD current, 5V) ∗1
- Package ..... QFP13-64pin, Die form

∗1: Add to current consumption

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