

EPSON ASIC Designs

ASICs - Move On To The Better Choice

Your application benefits from EPSON ASIC designs

- Higher Performance through Improved Technology
- Space saving and cost saving through higher integration
- Support of conversions of all major FPGA netlists
- Low minimum quantity orders (starting from 5,000 units)
- Low Turn Around Time for Test Sample Production from 3 weeks
- Two European Design Centers

- Leading Gate Array Technologies down to 0.25µm with:
 - IP cores (UART, LCD Contr., USB, IrDA etc.)
 - Support of one/two port RAM
 - Low Power at various voltage levels
 - Many Package Options
 - Range from 2K to 1.9m usable Gates

Get more information about ASICs at www.epson-electronics.de/asic or asic@epson-electronics.de



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THE POWER TO CREATE

