

**EPSON EUROPE ELECTRONICS GMBH**

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**EPSON****ASIC Inquiry Sheet****GENERAL INFORMATION**

Customer		City / Country	
Contact Person		Telephone	
Distributor		Contact Person	
Project name			
Application			

**TECHNICAL INFORMATION**

CMOS Technology	<input type="checkbox"/> 0.25 $\mu$ m	<input type="checkbox"/> 0.35 $\mu$ m	<input type="checkbox"/> 0.6 $\mu$ m	<input type="checkbox"/> 1.0 $\mu$ m
	<input type="checkbox"/> Gate Array	<input type="checkbox"/> Embedded Array	<input type="checkbox"/> Standard Cell	

Power Supply	Core Voltage		I/O Voltage	
	If core and I/O voltage levels are different, choose one of these options: <input type="checkbox"/> External dual power supply <input type="checkbox"/> Tolerant inputs, level: <input type="text"/>			
Package		Pin count		
Number of functional I/O pins			Number of power supply pins	
User Logic: max. number of usable gates				
Operating temperature				

SRAM	Block count	Word count x word length (e.g. 254 x 8 bit)	Specification	
			<input type="checkbox"/> 1 port	<input type="checkbox"/> synchronous
			<input type="checkbox"/> 2 port	<input type="checkbox"/> asynchronous
ROM	Word count x word length (eg 254 x 8 bit)			
FLASH	Total size in Kbit		Organization	<input type="checkbox"/> 8 bit <input type="checkbox"/> 16 bit

*NOTES: Specify additional memory blocks under remarks**No mask ROM or FLASH for G/A avail.***INTERFACES** (Please specify)**MACROCELLS** (Please specify)**REMARKS****COMMERCIAL INFORMATION**

Requested Schedule			
Sign Off		Samples	Mass Production
Quantity / year			
Quantity of additional engineering samples (20 with NRE)			
Target price per unit			
Target price for NRE			

**Shadowed information is mandatory for a feasibility studie**