EPSON

E0C33 Family MON33

Debug monitor

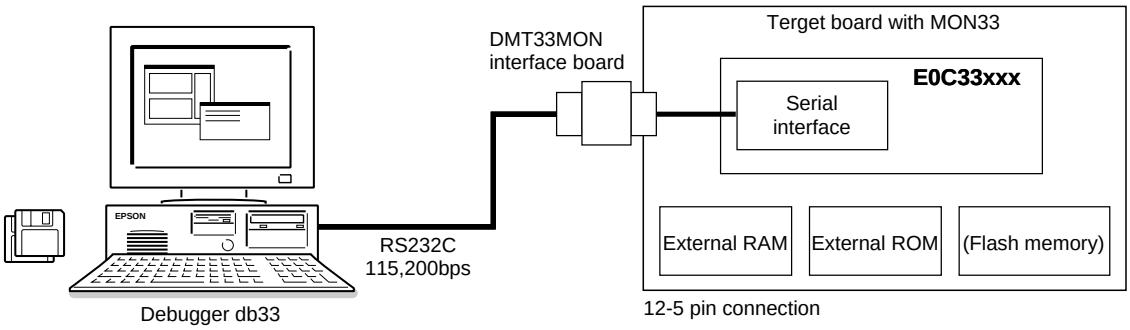
- Debug monitor for the E0C33 Family
- Runs on the target board providing a low-cost debug environment
- Controlled with the debugger in a PC through the DMT33MON I/F board

■ FEATURES

- MON33 is the debug monitor program for the E0C33 Family.
- Works on the user board by linking with the user program.
- Approx. 10KB ROM, approx. 2.5KB RAM and 1 ch of SIO are required for MON33 operation. No interrupt resource is used.
- The debug monitor on the target board is controlled by the db33 debugger in the PC connected via the DMT33 MON board with the RS232C cable. DMT33MON for 5V system and DMT33MONLV for 3.3V system are available. They support 115,200bps data transfer, and are connected to the user board with 12 pins (standard) or 5 pins (minimum).
- MON33 supports basic debug functions such as program downloading to RAM and Flash memory, memory dump, register read/write, stepping, program running, and PC break function.
- The MON33 middleware including all source codes is provided with a 3.5-inch FD. Simple I/F method of MON33 allows easy implementation with the user program.
- The debugging environment does not need the ICE. The MON33 debugging environment can be implemented to the final product.

■ DESCRIPTION

● Hardware System



● Software System

User software	Application			
MON33 library	**MON33 library** Start with m_mon_start (init functions)			RS232C Initialize, Read/Write
Hardware	E0C33 chip	ROM 10KB	RAM 2.5KB	SIO–DMT33MON

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